	CLOVER DISPLAY LTD.											
LCD MODULE SPECIFICATION Model : CV4202A												
	Revision	07										
	Engineering	Timmy Kwan										
	Date	21 April 2008										
	Our Reference	4202										
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E-MAIL : <u>cdl@cloverdisp</u>	lay.com											
URL : <u>http://www.clov</u>	verdisplay.com											

MODE OF DISPLAY

Display mode TN positive	Display condition Reflective type 	Viewing direction
TN negative	Transflective type	□ 12 O' clock
STN : 🗌 Yellow green	Transmissive type	\Box 3 O' clock
Grey	Others	9 O' clock
Blue (negative)		
☐ FSTN positive		
☐ FSTN negative		
LCD MODULE NUMBER	NOTATION:	
<u>CV4202A</u> - <u>M Y</u> - <u>S F</u> -	$\underline{N} \underline{6} - \underline{T} \qquad *(1) \text{Mode}$	l number of standard LCD Modules
	*(2)Backl	ight type
$(1) \qquad (2) (3) (4) (5) ($	6) (7) (8)	N – No backlight

- E EL backlight
- L Side-lited LED backlight
- M– Array LED backlight
- C CCFL

*(3)---Backlight color

- N No backlight
- A-Amber
- B Blue
- O– Orange
- W–White
- Y Yellow green
- *(4)---Display mode
 - T TN
 - V TN (Negative)
 - S STN Yellow green
 - G STN Grey
 - B STN Blue (Negative)
 - F FSTN
 - N FSTN (Negative)
- *(5)---Rear polarizer type
 - R Reflective
 - F Transflective
 - T Transmissive
- *(6)---Temperature range
 - N Normal
 - W– Extended
- *(7)---Viewing direction
 - 6 6 O'clock
 - 2 12 O'clock
 - 3 3 O'clock
 - 9 9 O'clock
- *(8)---Special code for other requirements
 - (Can be omitted if not used)
 - T Touch panel (Analog)
 - P Touch panel (Digital)

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GENERAL DESCRIPTION

Display mode	:	20 characters x 2 lines LCD module
Interface	:	4-bit or 8-bit parallel
Driving method	:	1/16 duty, 1/5 bias
Controller IC	:	Sitronix ST7066U or equivalent For the detailed information, please refer to the IC specifications

MECHANICAL DIMENSIONS

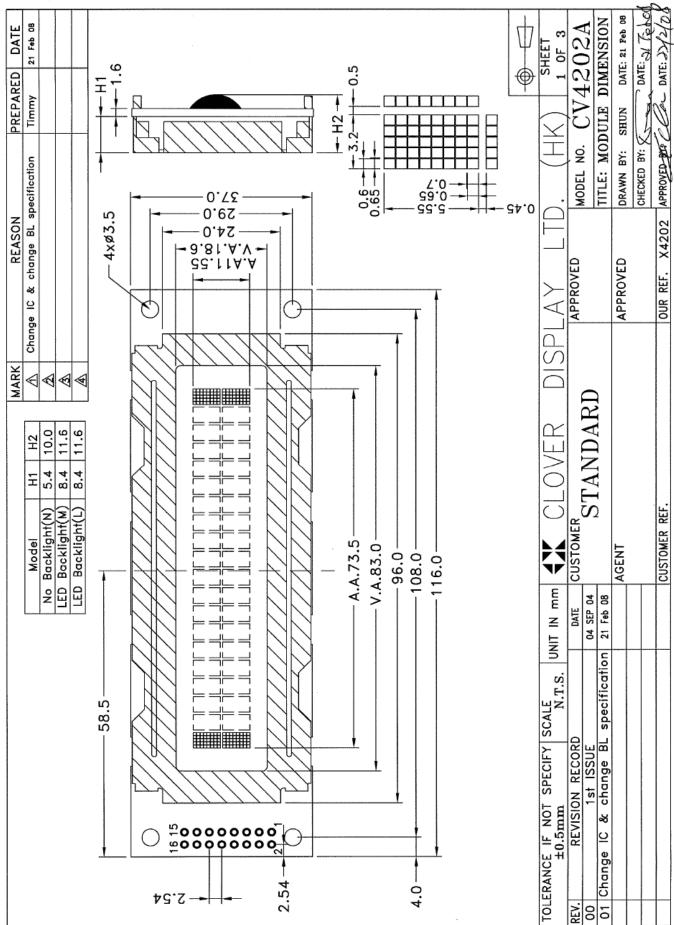
Item	Dimension		Unit	Item Dimension			Unit
Outline Dimension	116.0(L)x3	7.0(W)x (H1/H2)	mm	Character Pitch	3.70(W)x6.	0(W)	mm
Viewing Area	83.0(L)x18	.6(W)	mm	Dot Size	0.60(L)x0.6	55(W)	mm
Character Size	3.20(L)x5.5	55(W)	mm	—		_	—
No Backlight (N)	H1	5.4	mm	Side Backlight (L)	H1	8.4	mm
	H2	10.0	mm		H2	11.6	mm
EL Backlight (M)	H1	—	mm	Array Backlight (M)	H1	8.4	mm
	H2	_	mm		H2	11.6	mm

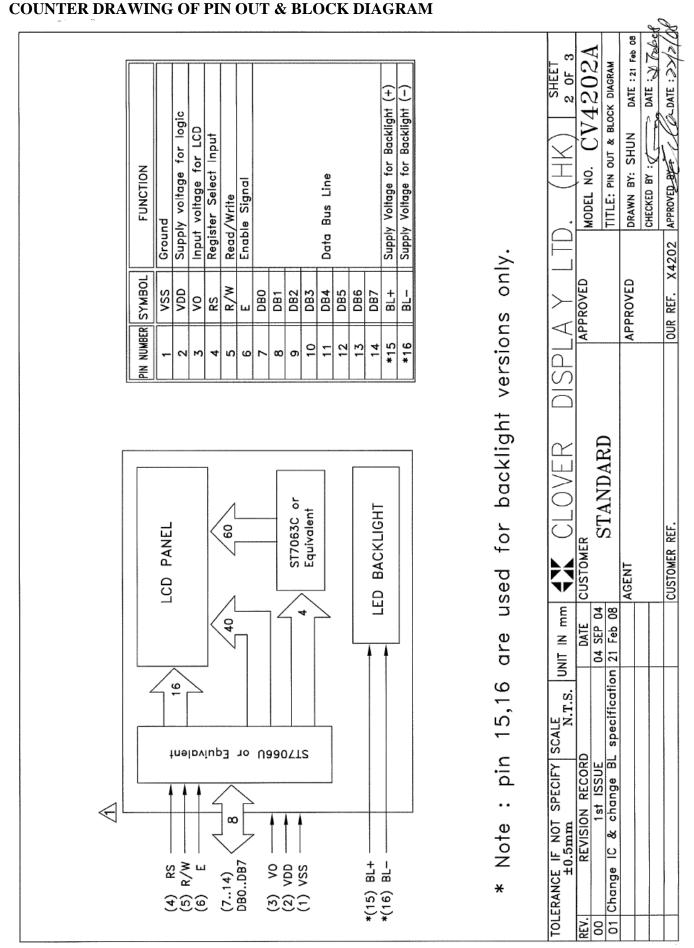
CONNECTOR PIN ASSIGNMENT

Pin No.	Symbol	Function	Pin No.	Symbol	Function
1	Vss	Ground	9	DB2	
2	VDD	Supply voltage for logic	10	DB3	
3	V 0	Input voltage for LCD	11	DB4	Data Bus Line
4	RS	Register Select Input	12	DB5	
5	R/W	Read/Write	13	DB6	
6	Е	Enable Signal	14	DB7	
7	DB0	Data Bus Line	*15	BL+	Supply voltage for backlight (+)
8	DB1	Data Bus Lille	*16	BL -	Supply voltage for backlight (-)

Note (*): Pin 15, 16 are used for backlight version

COUNTER DRAWING OF MODULE DIMENSION





DDAWING OF DIN OUT & DI OCK DIACDAM

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ELECTRICAL CHARACTERISTICS Conditions: VSS=0V, Ta=25°C													
Item	Symbol	MIN.	TYP.	MAX.	Unit	Item	Symbol	MIN.	TYP.	MAX.	Unit		
Supply Voltage	VDD	4.75	5.00	5.25	V	"H"Level Input Voltage	VIH	0.7VDD	_	VDD	V		
Supply Current	IDD	_	1.2	2.0	mA	"L" Level Input Voltage	VIL	-0.3		0.6	V		
Input Voltage for LCD	V0	-0.2	0	0.2	v	—	_			_	_		
Backlight Voltage						Backlight Current							
EL (@ Frequency 400Hz) VEL		_		_	Vrms	_	_	_		_			
Side-lited LED						Side-lited LED							
White	VBL	_	_	_	v	White	IBL				mA		
Blue	VBL	_	_	_	V	Blue	IBL	_		—	mA		
Yellow Green	VBL	_	5.0	5.2	V	Yellow Green	IBL	_	80	100	mA		
Array LED	•	•		•	•	Array LED				•			
Yellow Green	VBL	_	4.05	4.25	V	Yellow Green	Ibl	—	180	360	mA		
Amber	VBL	_	_	_	V	Amber	IBL	_		_	mA		
Orange	VBL	_	_	_	v	Orange	IBL	_		_	mA		
Red	VBL	_	_	_	v	Red	Ibl		_	_	mA		

ABSOLUTE MAXIMUM RATINGS

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	Vdd	-0.3 to7	-0.3 to 7	V
Input Voltage	VT	-0.3 to VDD +0.3	-0.3 to VDD +0.3	V
Operating Temperature	Topr	0 to 50	-20 to 70	°C
Storage Temperature	Tstg	-10 to 60	-30 to 80	°C

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INSTRUCTIONS

				Inst	ructi	on C	Code	;				Description
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Time (270КНz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.52 ms
Return Home	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.52 ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	s	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.	37 us
Display ON/OFF	0	0	0	0	0	0	1	D	с	В	D=1:entire display on C=1:cursor on B=1:cursor position on	37 us
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	37 us
Function Set	0	0	0	0	1	DL	N	F	x	x	DL:interface data is 8/4 bits N:number of line is 2/1 F:font size is 5x11/5x8	37 us
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter	37 us
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter	37 us
Read Busy flag and address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 us
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM)	37 us
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM)	37 us

Note:

Be sure the ST7066U is not in the busy state (BF = 0) before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to Instruction Table for the list of each instruction execution time.

DISPLAY DD RAM AND CHARACTER POSITION

20x2, 1/16 DUTY CYCLE

	1	2		20	DI
			••••••••		
line 2	40	41	••••••••	53	

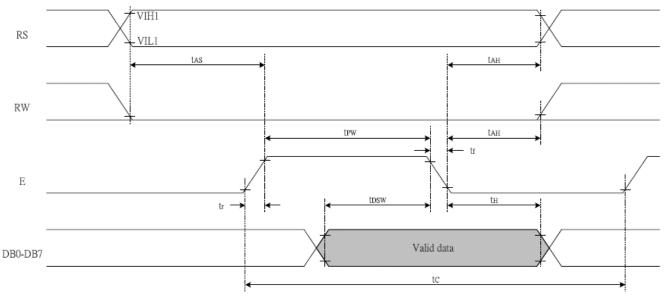
DISPLAY POSITION DD RAM ADDRESS

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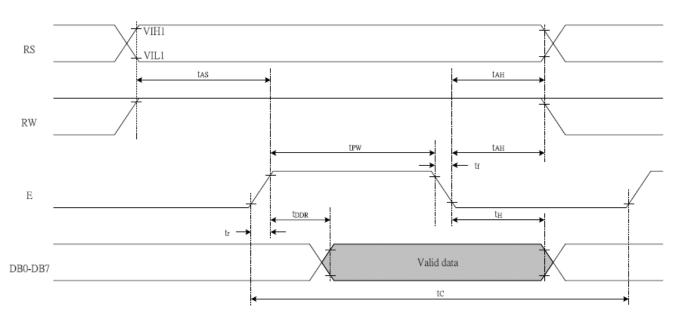
TIMING CHARACTERISTICS OF COMPATIBLE CONTROLLER CHIPS TA = 25° C , VCC = 5V

<u> </u>	R = 25 C, VCC = 5 V											
	Write Mode (Writing data from MPU to ST7066U)											
Tc	Enable Cycle Time	Pin E	1200	-	-	ns						
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns						
T_R,T_F	Enable Rise/Fall Time	Pin E	-	-	25	ns						
T _{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns						
T _{ah}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns						
T _{DSW}	Data Setup Time	Pins: DB0 - DB7	40	-	-	ns						
Tн	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns						
	Read Mode	e (Reading Data from ST70	66U to N	APU)								
Tc	Enable Cycle Time	Pin E	1200	-	-	ns						
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns						
T _R ,T _F	Enable Rise/Fall Time	Pin E	-	-	25	ns						
T _{AS}	Address Setup Time	Pins: RS,RW,E	0	-	-	ns						
T _{AH}	Address Hold Time	Pins: RS,RW,E	10	-	-	ns						
T _{ddr}	Data Setup Time	Pins: DB0 - DB7	-	-	100	ns						
Τ _H	Data Hold Time	Pins: DB0 - DB7	10	-	-	ns						

Write Mode Timing Diagram (Writing Data from MPU to ST7066U)



Read Mode Timing Diagram (Reading Data from ST7066U to MPU)

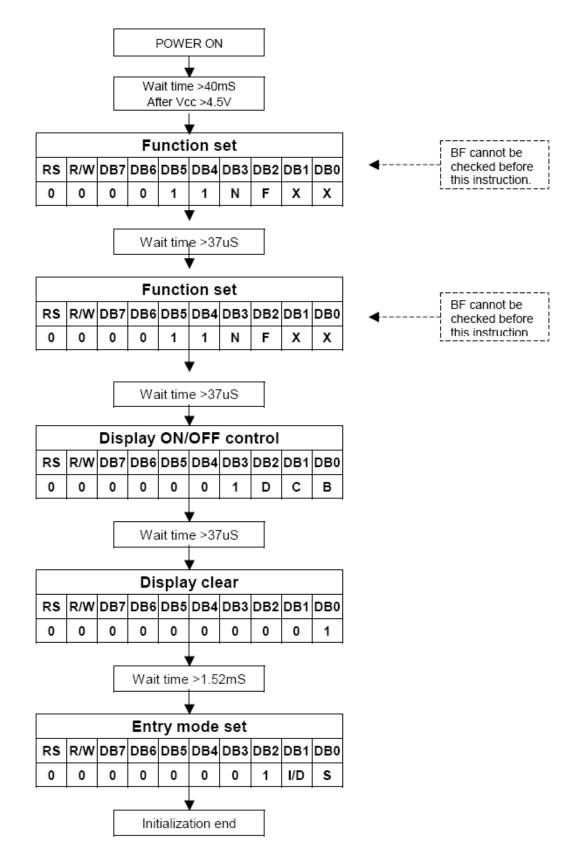


THE RESET CIRCUIT

An internal reset circuit automatically initializes the ST7066U when the power is turned on. The following instructions are executed during the initialization. The busy flag (BF) is kept in the busy state until the initialization ends (BF = 1). The busy state lasts for 40 ms after VCC rises to 4.5 V.

- 1. Display clear
- 2. Function set:
 - DL = 1; 8-bit interface data
 - N = 0; 1-line display
 - F = 0; 5x8 dot character font
- 3. Display on/off control:
 - D = 0; Display off
 - C = 0; Cursor off
 - B = 0; Blinking off
- 4. Entry mode set:
 - I/D = 1; Increment by 1
 - S = 0; No shift

Initialization for 8-Bit Interface



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Initialization for 4-Bit Interface

	or 4-B											
				F	POWE		J					
				Wa	ait tim	e >40						
					ter Vo							
					,	r						
						ion s						BF cannot be
	RS	<u> </u>	DB7	DB6	DB5	DB4	-		<u> </u>		. ◀	checked before
	0	0	0	0	1	1	Х	Х	Х	X		this instruction.
				Wa	ait tim							
						r						
		-				ion s		-			_	BF cannot be
	RS			DB6	DB5							checked before
	0	0	0	0	1	0	X	X	X	X		this instruction.
	0	0	Ν	F	Х	X	Х	Х	Х	X		
				Wa		•	7C					
				844	ait um	e >37	/uS					
						•					r	
						on s						BF cannot be
	RS			DB6	-							checked before
	0	0	0 N	0 F	1	0	X	X	X	X		this instruction
	U	U	N	Г	X	X	Х	Х	Х	X		
				We	it tim	0 > 27	7					
				Wa	ait tim	e >37	7uS					
					,						ſ	
	DO	DAM		play	ON/	OFF	cor				[
	RS	L	DB7	olay DB6	ON/ DB5	OFF DB4	cor DB3	DB2	DB1			
•	0	0	DB7 0	olay DB6 0	ON/ DB5 0	OFF DB4 0	cor DB3 X	DB2 X	DB1 X	Х		
		L	DB7	olay DB6	ON/ DB5	OFF DB4	cor DB3	DB2	DB1		[
	0	0	DB7 0	olay DB6 0 D	ON/ DB5 0 C	OFF DB4 0	COR DB3 X X	DB2 X	DB1 X	Х		
	0	0	DB7 0	olay DB6 0 D	ON/ DB5 0 C	OFF DB4 0 B	COR DB3 X X	DB2 X	DB1 X	Х	-	
•	0	0	DB7 0	DB6 0 D Wa	ON/ DB5 0 C	OFF DB4 0 B e >37	COR DB3 X X TuS	DB2 X	DB1 X	Х		
•	0	0	DB7 0 1	DB6 0 D Wa	ON/ DB5 0 C	OFF DB4 0 B e >37	TuS	DB2 X X	DB1 X X	X X		
•	0 0 RS	0	DB7 0 1	DB6 0 D Wa	ON/ DB5 0 C	OFF DB4 0 B e >37	TuS	DB2 X X DB2	DB1 X X DB1	X X DB0		
•	0	0 0 R/W	DB7 0 1 DB7	DB6 0 D Wa DB6	ON/ DB5 0 C , ait tim spla DB5	OFF DB4 0 B e >37 y cle DB4	TuS	DB2 X X	DB1 X X	X X		
•	0 0 RS 0	0 0 R/W 0	DB7 0 1 0 DB7 0	DB6 0 D Wa DB6 0 0	ON/ DB5 0 C iiit tim Spla 0 0 0	OFF DB4 0 B e >37 y cle DB4 0 1	TuS	DB2 X X DB2 X	DB1 X X DB1 X	X X DB0 X		
	0 0 RS 0	0 0 R/W 0	DB7 0 1 0 DB7 0	DB6 0 D Wa DB6 0 0	ON/ DB5 0 C iiit tim Spla 0 0 0	OFF DB4 0 B e >37 y cle DB4 0	TuS	DB2 X X DB2 X	DB1 X X DB1 X	X X DB0 X		
	0 0 RS 0	0 0 R/W 0	DB7 0 1 0 DB7 0	DB6 0 D Wa DB6 0 0 Wait	ON/ DB5 0 C spla DB5 0 0 0	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5	TuS	DB2 X X DB2 X	DB1 X X DB1 X	X X DB0 X		
	0 0 RS 0 0	0 0 8/W 0	DB7 0 1 0 0 0	DB6 0 D Wa DB6 0 0 Wait	ON/ DB5 0 C it time 0 0 0 0 c t time	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5	TuS	DB2 X X DB2 X X	DB1 X X DB1 X X	X X DB0 X X		
•	0 0 RS 0 0	0 0 R/W 0 0	DB7 0 1 DB7 0 0	DB6 0 D Wa DB6 0 0 Wait DB6	ON/ DB5 0 C it time 0 0 0 t time DB5	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5 >1.5	TuS	DB2 X X DB2 X X X DB2	DB1 X X DB1 X X DB1	X X DB0 X X X		
•	0 0 RS 0 0 8 S 0	0 0 R/W 0 0	DB7 0 1 DB7 0 0 DB7 0	DB6 0 D Wa DB6 0 Wait DB6 0 0 0	ON/ DB5 0 C spla DB5 0 0 titime DB5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5 >1.5 Ode DB4 0	TuS TuS TuS TuS TuS TuS TuS TuS TuS TuS	DB2 X X DB2 X X DB2 X	DB1 X X DB1 X X DB1 X	X X DB0 X X DB0 X		
	0 0 RS 0 0	0 0 R/W 0 0	DB7 0 1 DB7 0 0	DB6 0 D Wa DB6 0 0 Wait DB6	ON/ DB5 0 C it time 0 0 0 t time DB5	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5 >1.5	TuS	DB2 X X DB2 X X X DB2	DB1 X X DB1 X X DB1	X X DB0 X X X		
	0 0 RS 0 0 8 S 0	0 0 R/W 0 0	DB7 0 1 DB7 0 0 DB7 0	DB6 0 D Wa DB6 0 0 Wait DB6 0 1	ON/ DB5 0 C witt time DB5 0 0 C Ty m DB5 0 I/D	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5 >1.5 Ode DB4 0 S	Cor DB3 X X TuS TuS Cor DB3 X X 2mS Set DB3 X X X	DB2 X X DB2 X X DB2 X	DB1 X X DB1 X X DB1 X	X X DB0 X X DB0 X		
· · · · · · · · · · · · ·	0 0 RS 0 0 8 S 0	0 0 R/W 0 0	DB7 0 1 DB7 0 0 DB7 0	DB6 0 D Wa DB6 0 0 Wait DB6 0 1	ON/ DB5 0 C witt time DB5 0 0 C Ty m DB5 0 I/D	OFF DB4 0 B e >37 y cle DB4 0 1 >1.5 >1.5 Ode DB4 0	Cor DB3 X X TuS TuS Cor DB3 X X 2mS Set DB3 X X X	DB2 X X DB2 X X DB2 X	DB1 X X DB1 X X DB1 X	X X DB0 X X DB0 X		

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ELECTRO-OPTICAL CHARACTERISTICS

MEASURING CONDITION:

POWER SUPPLY = VOP / 64 Hz TEMPERATURE = 23 ± 5 °C RELATIVE HUMIDITY = 60 ± 20 %

ITEM	SYMBOL	UNIT	TYP. TN	TYP. STN
RESPONSE TIME	Ton	ms	130	150
	Toff	ms	170	190
CONTRAST RATIO	Cr	-	8	15
	V3:00	0	70	45
VIEWING ANGLE (6 O'clock)	V6:00	0	45	70
$(Cr \ge 2)$	V9:00	0	70	45
	V12:00	0	5	60

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

RELIABILITY OF LCD MODULE

	TEST CONDITION	TEST CONDITION	
ITEM	FOR NORMAL TEMPERATURE	FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C	-30°C to 80°C	5 cycles
	30 Min Dwell	30 Min Dwell	
Vibration Test at LCM Level	Freq 10-55 Hz	Freq 10-55 Hz	
	Sweep rate: 10-55-10 at 1 min	Sweep rate: 10-55-10 at 1 min	
	Sweep mode Linear	Sweep mode Linear	—
	Displacement: 2 mm p-p	Displacement: 2 mm p-p Displacement: 2 mm p-p	
	1 Hour each for X, Y, Z	1 Hour each for X, Y, Z	

QUALITY STANDARD OF LCD MODULE

1.0	Sampling Method
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1.0	0 Sampling Method					
	Sampling Plan : MIL	Sampling Plan : MIL STD 105 E				
	Class of AQL : Level II/Single Sampling					
	Critical : 0.25% Major 0.65% Minor 1.5%					
2.0	Defect Group	Failure Category	Failure Reasons			
	Critical Defect	Malfunction	Open			
	0.25%(AQL)		Short			
			Burnt or dead component			
			Missing part/improper part P.C.B.			
			Broken			
	Major Defect	Poor Insulation	Potential short			
	0.65%(AQL)		High current			
			Component damage or scratched			
			or Lying too close improper coating			
		Poor Conduction	Damage joint			
			Wrong polarity			
			Wrong spec. part			
			Uneven/intermittent contact			
			Loose part			
			Copper peeling			
			Rust or corrosion or dirt's			
	Minor Defect	Cosmetic Defect	Minor scratch			
	1.5%(AQL)		Flux residue			
			Thin solder			
			Poor plating			
			Poor marking			
			Crack solder			
			Poor bending			
			Poor packing			
L			Wrong size			

HANDLING PRECAUTIONS

(1) CAUTION OF LCD HANDLING & CLEANING

The polarizing plate on the surface of the panel is made from organic substances. Be very careful for chemicals not to touch the plate or it leads the polarizing plate to deteriorate.

If the use of a chemical is unavoidable, wipe the panel lightly with soft materials, such as gauze and absorbent cotton, soaked in a solvent.

*Usable solvent: Alcohol (ethanol, IPA and the like) *Appropriate solvent: Ketones, ethyl alcohol

Avoid wiping with a dry cloth, since it could damage the surface of the polarizing plate and others.

(2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommended that any unused input terminal would be connected to V_{DD} or V_{SS} , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

(3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed to direct sunshine or high temperature/humidity for long periods.

(4) CAUTION FOR OPERATION

The viewing angle can be adjusted by varying the LCD driving voltage VO.

Driving voltage should be kept within specified range, excess voltage shortens display life.

Response time increases with decrease in temperature.

Display may turn black or dark Blue at temperature above its operational range; this is however not destructive and the display will return to normal once the temperature falls back to range.

Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured". They will recover once the display is turned off.

Condensation at terminals will cause malfunction and possible electrochemical reaction. Relative humidity of the environment should therefore be kept below 60%.

(5) SAFETY

Liquid crystal may leak out of a damaged LCD, it is recommended to wash off the liquid crystal by using solvents such as acetone or ethanol and should be burned up later.

If any liquid leaks out of a damaged glass cell comes in contact with your hands, wash it off with soap and water immediately.

WARRANTY

CLOVER will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Clover is limited to repair and/or replacement. Clover will not be responsible for any subsequent or consequential event.